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Polyimide Gate Dielectrics for Organic Thin Film Transistor

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Surface modification of gate insulators has been one of the key issues for a performance improvement of organic thin-film transistors (OTFTs). The modified surfaces of the gate insulators altered interactions between the organic semiconductors and the gate insulators, which affected a initial growth of the organic semiconductor on the gate insulator. Therefore, it has been believed that a well-defined interface control of the gate insulator and organic semiconductor is one of the most important parameters for a realization of stable, reliable and high performance organic thin film transistors. In this presentation, we propose a new method to achieve well-defined surface wetting properties of the polymeric gate dielectric without using a conventional SAM technique. To control the surface properties of the gate insulators, a soluble polyimide was prepared from 5-(2,5-dioxotetrahydrofuryl)-3-methyl-3-cyclohexene-1,2-dicarboxylic anhydride [DOCDA] and functional diamine, 1-(3,5-diaminophenyl)-3octadecyl-pyrrolidine-2,5-dione [DA-L-18IM], which was introduced into the gate insulators. Performance of the pentacene thin film transistors were obtained with a filed-effect-mobility of 0.14~0.94 cm²/Vs according to the type of the polymer components. Additionally, we had investigated a relationship between the surface properties (surface morphology, surface tension, etc) of the gate insulators and the performance of OTFTs, which will be discussed in more detail.

References

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